#### Agile development for big machines

### Holistic Lithography acceptation enables Rapid Application Development

Tom Hoogenboom, System Engineering, ASML

v2 | 6-oct-2015 | Mechelen

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#### Agile development for big machines

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## Holistic Lithography acceptation enables agile development

Wafer scanners are complex systems [ref1].

A system of wafer scanners, integrated in a 'fab' with metrology tools, reticle designs etc. is even more complex [ref2].

There is now a new 'holistic' environment enabling the industry to continue to pursue Moore's law in an agile way.

This is illustrated in this talk using stability control software as an example.

## Holistic Lithography acceptation enables agile development

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There is now a new 'holistic' environment enabling the industry to continue to pursue Moore's law in an agile\* way.

This is illustrated in this talk using stability control software as an example.

\*) solutions evolve through collaboration between self-organizing, cross-functional teams. Methods: adaptive planning, evolutionary development, early delivery. encourages rapid and flexible response to change.

[source: Wikipedia]

#### Summary

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The semiconductor industry demands continuous development

A holistic architecture enables agile application development

System Architecting is driving the agile development cycle

#### **Summary**

Public Slide 6 6-oct-2015

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## The semiconductor industry demands continuous development

The semiconductor industry is driven by Moore's law.

- Continuous shrink, now working with < 25 nm lines
- Economy of shrink drives continuous innovation
- Continuous innovation requires change
- Changes (e.g. new SW) can affect High Volume Production

Holistic Lithography opens up new areas for improvement:

- In Holistic Lithography all aspects
   of the wafer production process are optimized together [next slides]
- Changes can be introduced in production in a controlled way [last section]

## The scanner is the Central Processing Unit in a fab

Public Slide 8 8 February 2016

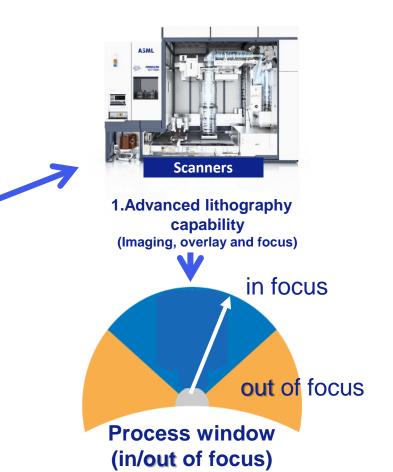


#### Scanner + mask determine the process window

mask

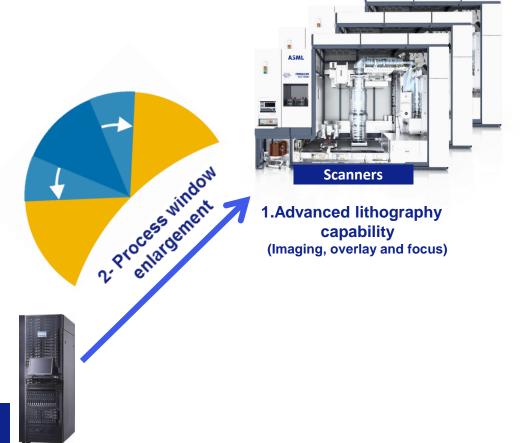
Product reticles

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Off-tool SW optimizes mask for max process window

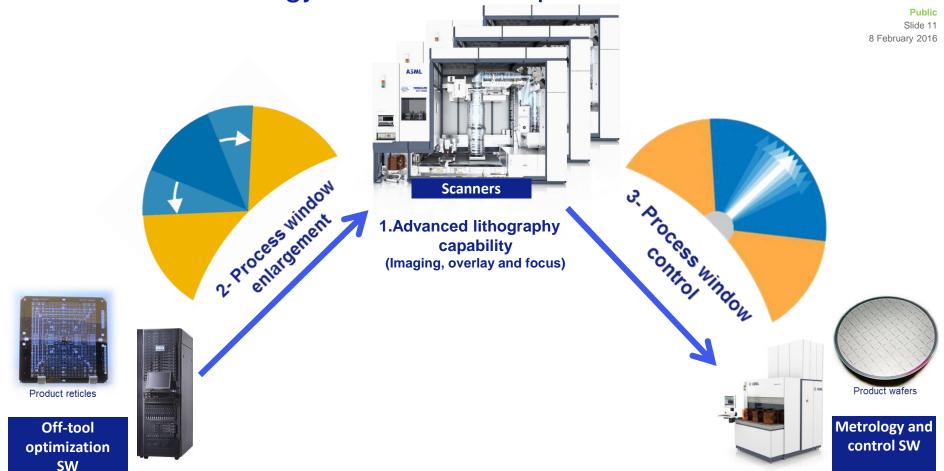
Public Slide 10 8 February 2016



Product reticles

Off-tool optimization SW

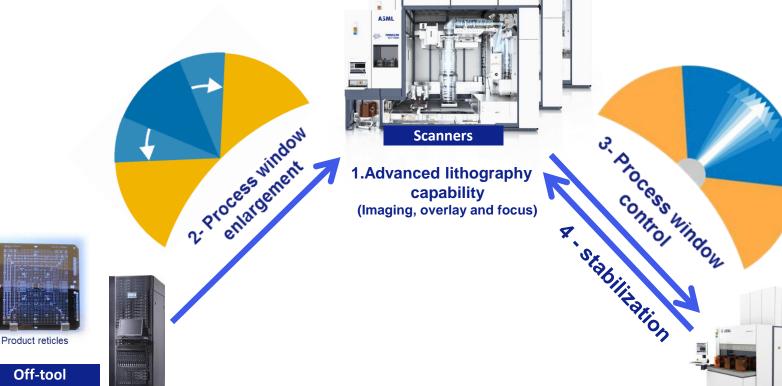
YieldStar metrology tool measures process window



Holistic Litho: mask design and measurements

stabilize scanner focus







Product wafers

**Metrology and** control SW

Off-tool optimization **SW** 

# Holistic Litho: improvements do not require to change the scanner but instead we use it in a better way

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The semiconductor industry demands continuous development

Moore's law continues in a holistic environment

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Application == nm gain

Scanners: Controlled change



Scanners: Controlled change



### Holistic SW platform: easier to change



Scanners: Controlled change



Holistic SW platform: easier to change



### Rapid Application Development



Nr. of releases / year

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#### Easier to change:

- More frequent, smaller trips
- Needs more Quality control
- But overall less effort and better results

### Holistic SW platform: easier to change

#### Scanners: Controlled change

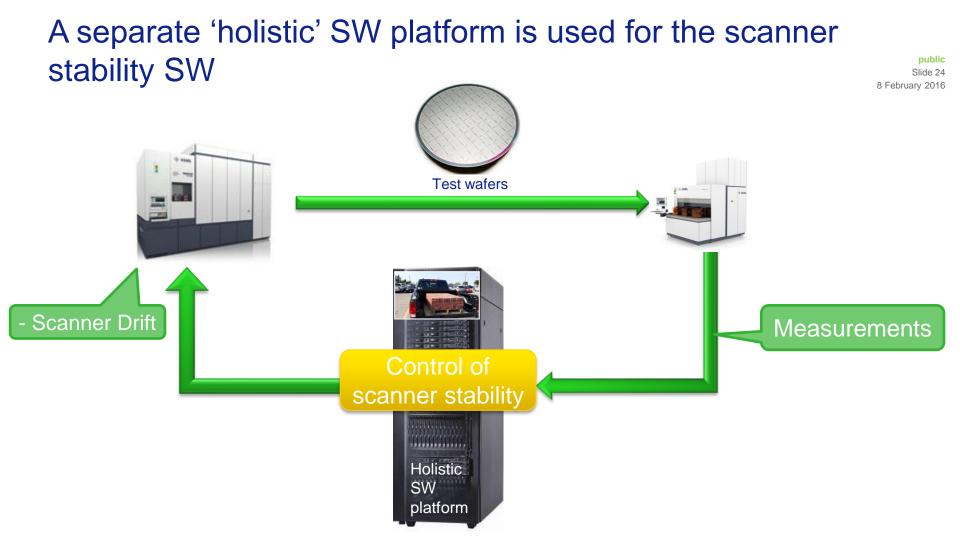




### Rapid Application Development



Nr. of releases / year



#### SW is now independent of scanner SW

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#### Stability control SW runs on an independent platform

- No direct link to scanner HW/SW
- Tested separately from scanners
- Integrated in customer environment



#### Still not super-rapid, because of the startup and integration steps:

- Find better stabilization algorithm & prove it works
  - adapt scanner stabilization control SW (agile/scrum)
    - integrate and test at beta site (including scanner integration)
      - Then release for all sites







#### Holistic architecture:

public Slide 26 production Scanners are stabilized by 8 February 2016 independent SW systems to enable agile development **Test wafers** Scanner Drift Measurements Control of scanner stability Holistic SW

platform

Application == nm gain

Scanners: Controlled change



Holistic SW platform: easier to change



### Rapid Application Development



Nr. of releases / year

Application == nm gain

Scanners: Controlled change



Holistic SW platform: easier to change



### Rapid Application Development



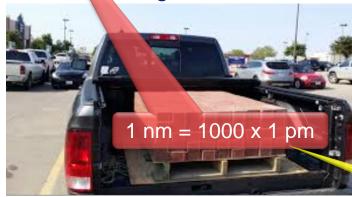
Nr. of releases / year

Application == nm gain

Scanners: Controlled change



Holistic SW platform: easier to change



### Rapid Application Development



Nr of pm / year

The semiconductor industry demands continuous development

Moore's law continues in a holistic environment

A holistic architecture enables agile application development Production Scanners are stabilized by independent SW systems

System Architecting is driving the agile development cycle

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## System Architecting is driving the development cycle

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The role of the System Architect is to set up a few pointers out there that help guide the developers into the right direction

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In the old days a scanner architect would go for the full 1000 pm

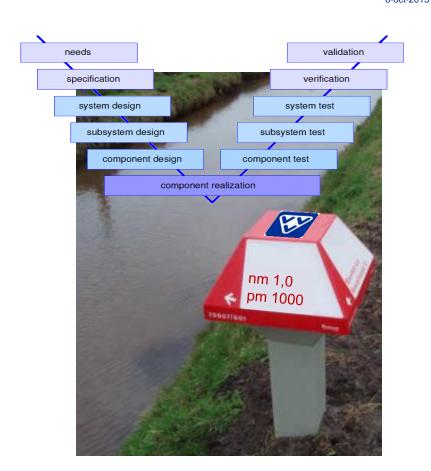


## System Architecting is driving the development cycle

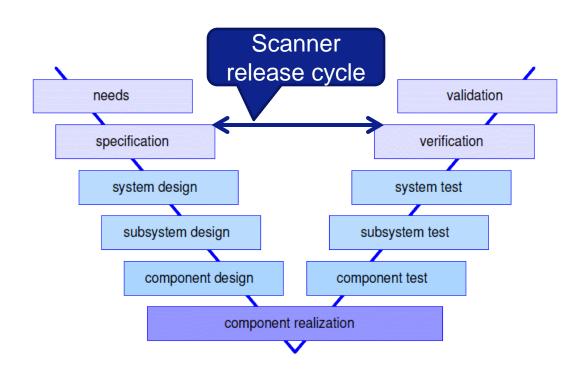
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Using the V-model [Ref3] to cross the waters



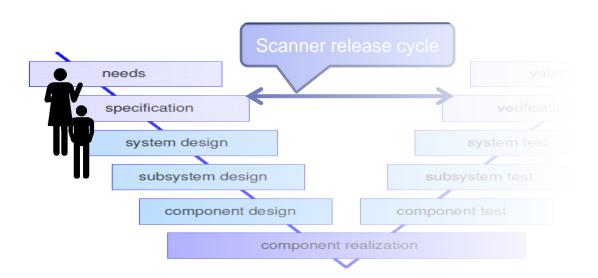
#### The scanner HW cycle is the leading $\mathbf{V}$ for 1 nm of gain:





#### Agile development adds two new key points:

 System architecting is done by a team End users are in the team and have a say in where the team is going

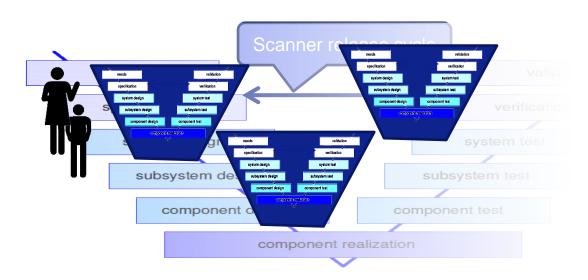


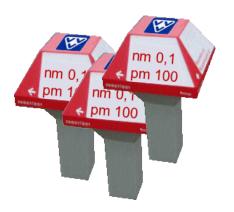


## System Architecting teams drive the agile development cycles

#### Agile development adds two new key points:

- System architecting is done by a team
   End users are in the team and have a say in where the team is going
- 2) Many smaller V's deliver a few 100 pm at the time





#### Conclusion: build an architecting team

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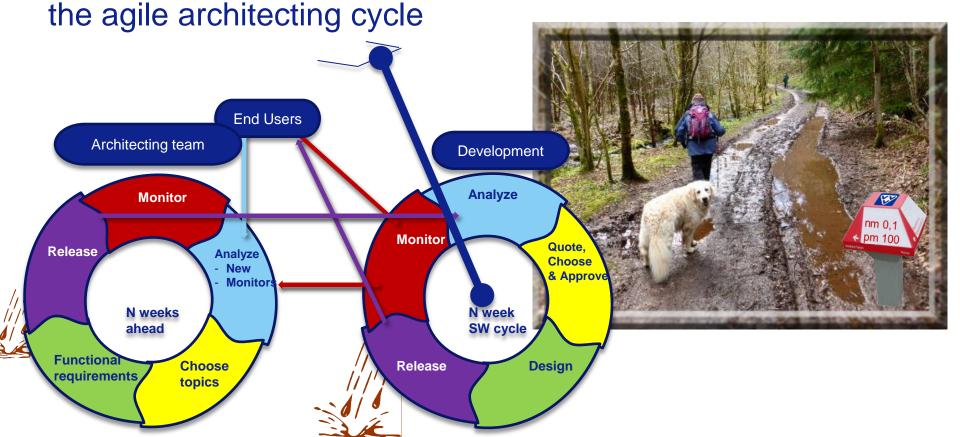
### Conclusion: build an architecting team and learn how to drive

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Conclusion: build an architecting team and learn how to drive

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The semiconductor industry demands continuous development

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A holistic architecture enables agile application development Production Scanners are stabilized by independent SW systems

System Architecting is driving the agile development cycle

The familiar engineering V model has become a vvv model

#### References

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| [Ref1] | The Waferstepper Challenge: Innovation and Reliability |
|--------|--|
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|        | http://www.gaudisite.nl/info/IRCwaferstepper.info.html |

[Ref2] ASML Holistic Lithography, ASML, http://www.asml.com/asml/show.do?ctx=38884

[Ref3] All About Systems Engineering; Introductory Course,
Gerrit Muller
<a href="http://www.gaudisite.nl/info/SEintroductionCourse.info.html">http://www.gaudisite.nl/info/SEintroductionCourse.info.html</a>