

Comparing Performance Analysis Methods on an Industrial Case Study

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BODERC Project



Carrying Industrial Partner:



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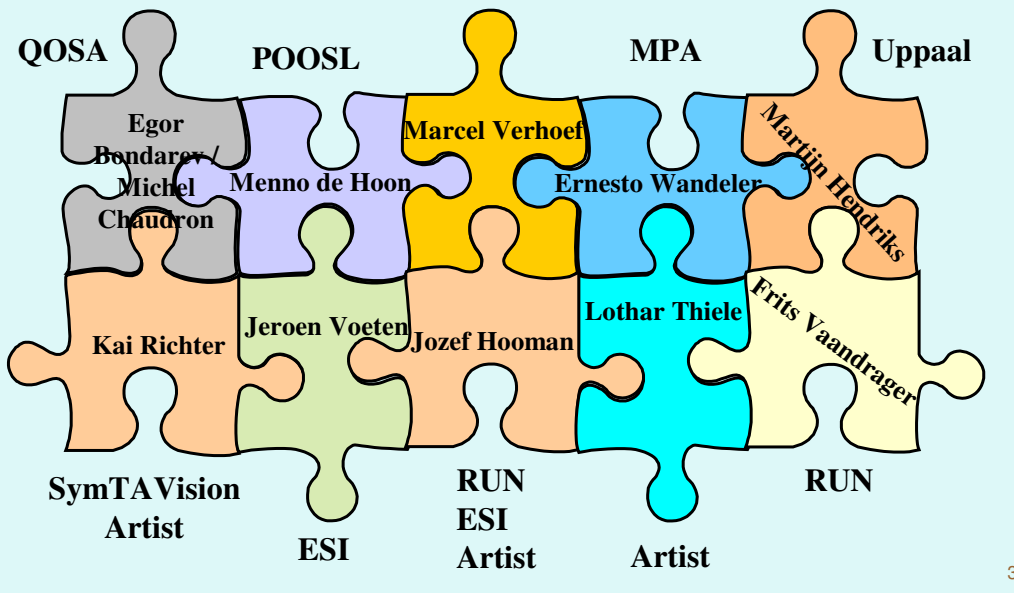


Aim: improve high-level design of
mechatronic systems

Includes

- multi-disciplinary design space
exploration, focus on performance
- analysis of system-level decisions
- predict consequences of design
decisions as early as possible

Collaboration on case study



Agenda

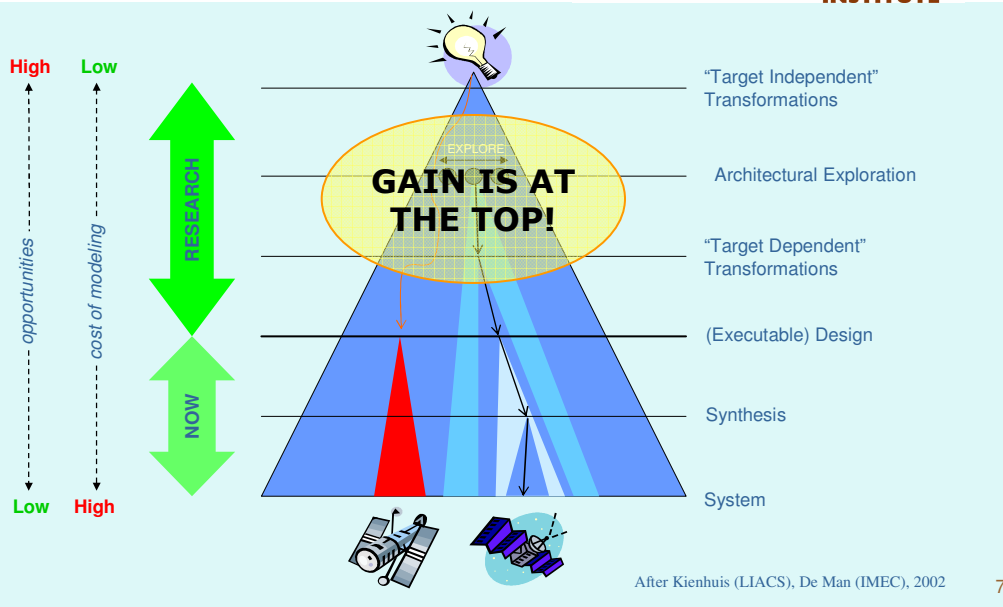
- Why comparison of techniques?
- Suitable benchmark
- Early results
- Lessons learnt

- **Why performance analysis?**
 - continuous increase in functionality demands
 - continuous drive to reduce cost price
 - tighter time-to-market demands
 - rapidly evolving technology
- **over dimensioning not longer viable (\$)**
- **need for early design choice impact analysis**
- **and continuous monitoring over life cycle**
- **still not always recognized in industry!**

“Does The Product Work?”



“Does The Product Work Given a Set of
Hard Resource Constraints?”



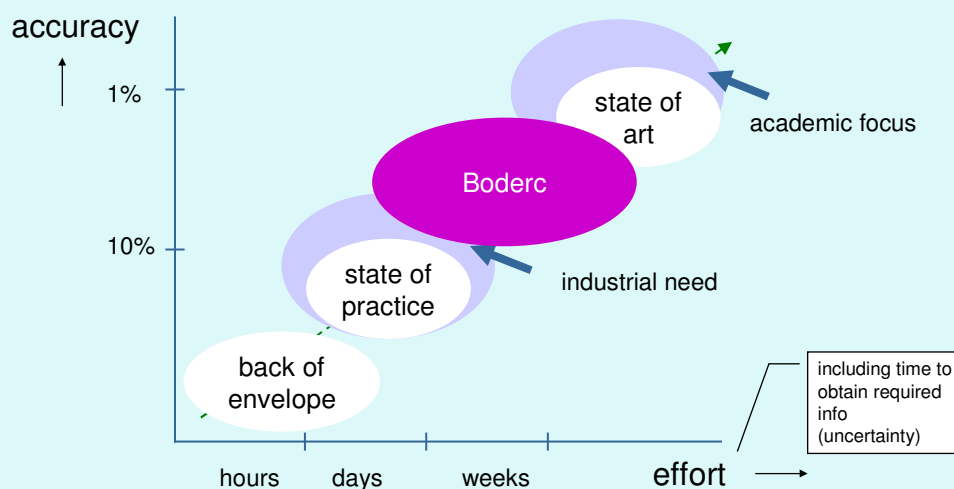
- finding quantitative answers in the early life cycle is very hard, there are many unknowns
- “shooting at a moving target”
- need for a light-weight approach that can deal with highly interactive nature of the design process

Why comparison?

- Trade-off between *effort* and *insight gained* not well understood
 - Investment: modeling effort
 - Investment: analysis effort
 - Return-on-investment: question answered? what accuracy?
 - Return-on-investment: question answered on time?
- Problems industry faces
 - Many techniques available (DES, QN, STOCH); which one fits my problem? How do I select the proper tool?
 - How steep is learning curve; do I need to become an expert?
 - Fit with design cycle; disruptive to current way of working?
 - Sufficient tool support?

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Overview performance models



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Aim of our research

- Understand pros *and cons* of techniques
- Build a taxonomy: problem ↔ methods
- Useful combinations?
- Compensate weakness of 'x' with strength of 'y' ?
- Fit in design cycle: early ↔ late, throughout?
- Fit in design process: how to introduce 'x'

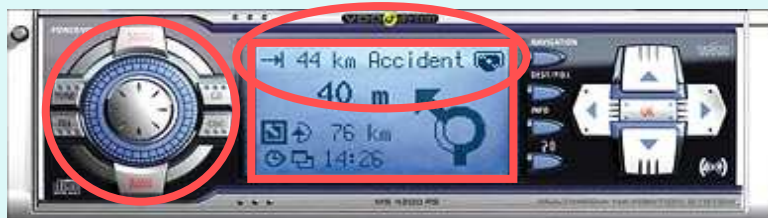
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Benchmarking

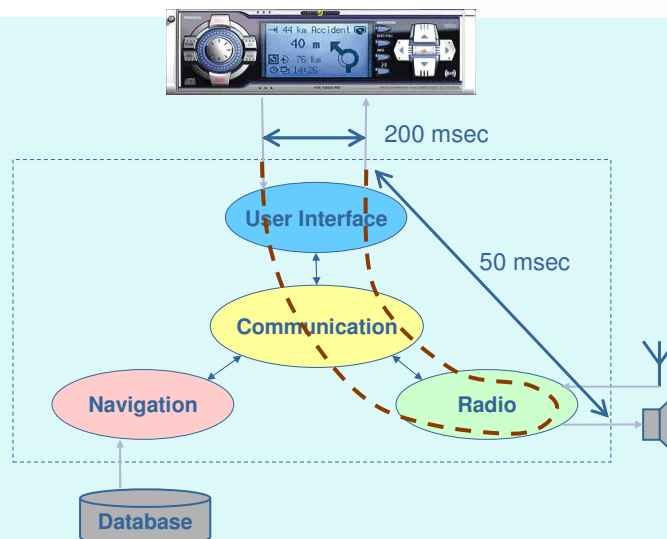
- Simple case, such that all techniques can deal with it
- Sufficiently complex to provoke problematic issues
- Extendable to introduce new “sub-problems”
- How to avoid “Lies, True Lies, Statistics” problem?

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- Car radio with built-in navigation system
- User interface needs to be responsive
- Traffic messages must be processed in a timely way
- Several applications may execute concurrently

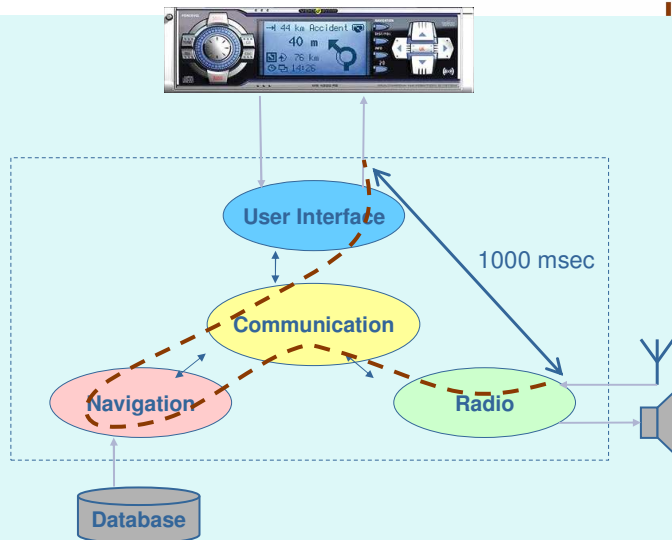
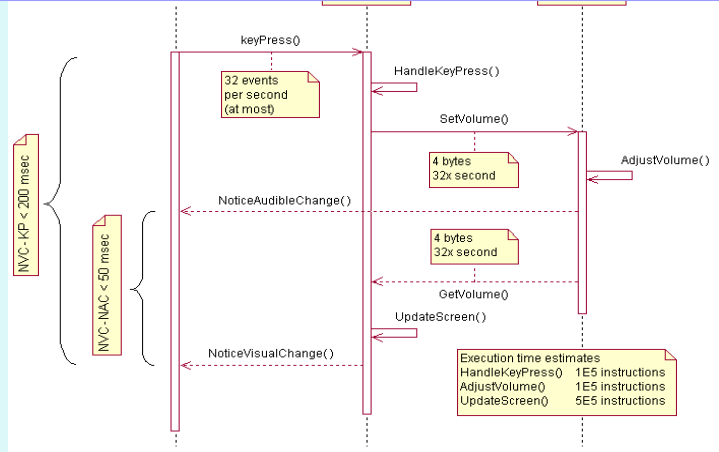


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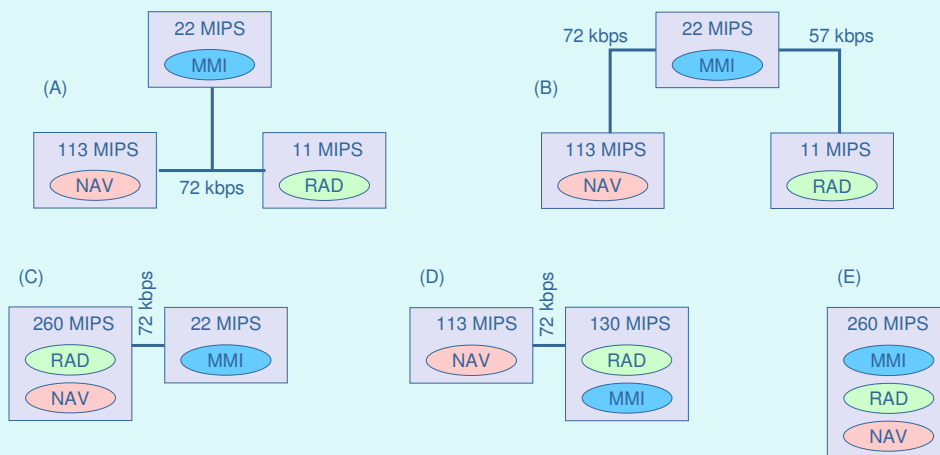


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Computation Resource Demand



Proposed Architecture Alternatives



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Analysis questions

1. How do the proposed system architectures compare in respect to end-to-end delays?
 2. How robust is architecture A? Where is the bottleneck of this architecture?
 3. Architecture D is chosen for further investigation. How should the processors be dimensioned?
- This presentation: focus on question 1 on architecture A

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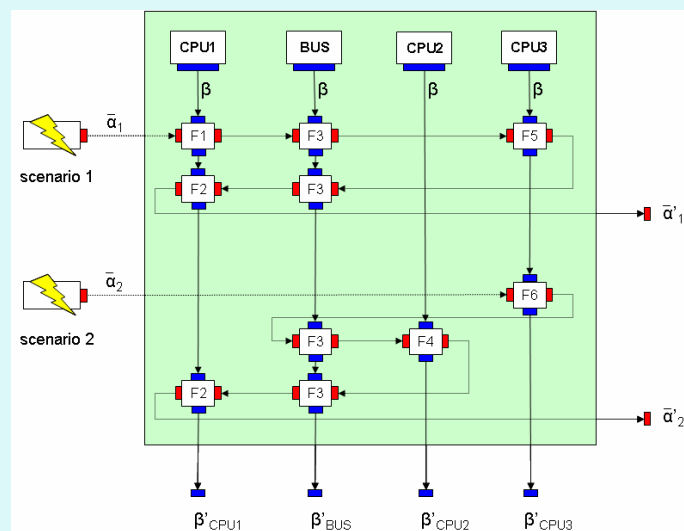
Method: MPA (1)

- **Modular Performance Analysis**
- **Developed at ETH Zurich (Lothar Thiele et al)**
- **Performance networks analysed with real-time calculus**
- **Analytic method, deterministic queuing theory**
- **Adaption of Network Calculus (Boudec, Thiran)**
- **Describes event streams by interval bound functions**
- **Information is lost: $t \rightarrow \Delta t$**
- **Evaluation is very fast (no simulation)**

- <http://www.mpa.ethz.ch>

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Method: MPA (2)



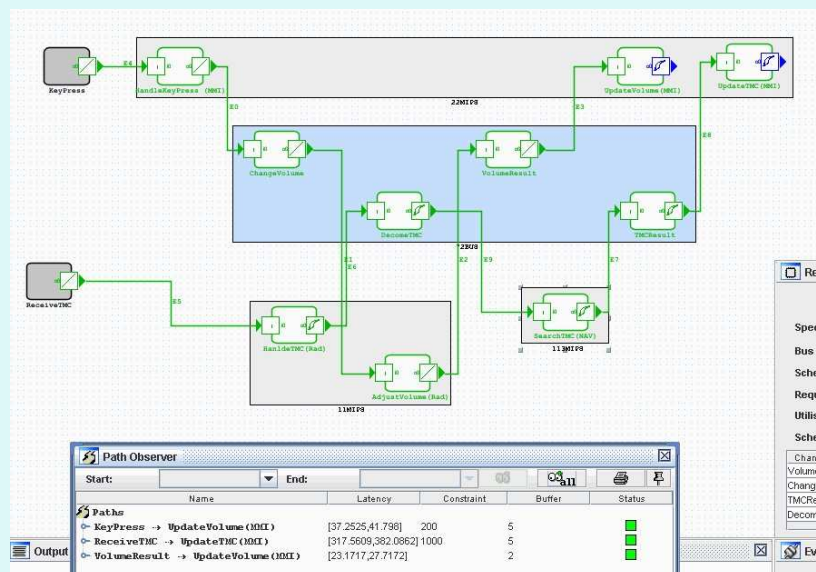
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Method: SymTA/S (1)

- Symbolic Timing Analysis for Systems
 - Developed at TU Braunschweig (Rolf Ernst et al)
 - Classical (formal) scheduling analysis techniques
 - Symbolic simulation
 - Calculate resource local optima
 - Optimize system level by iteration over local optima
 - Heterogeneous architectures
 - Complex task dependancies, context aware analysis
 - Rapid design space exploration by sensitivity analysis
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- <http://www.symtavisoin.com>

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Method: SymTA/S (2)



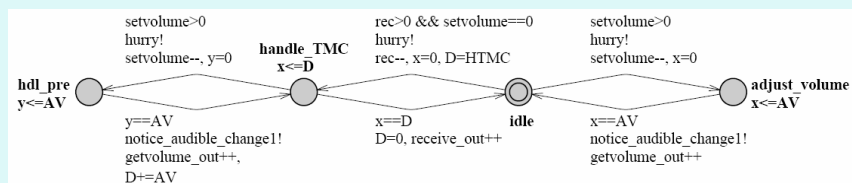
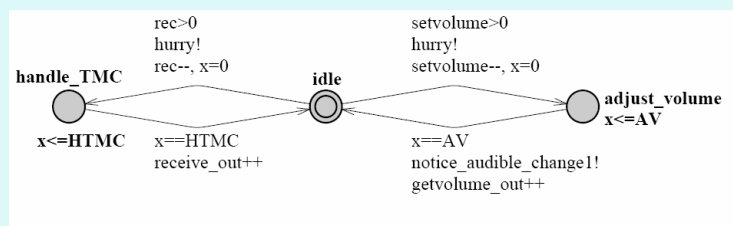
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Method: Uppaal (1)

- Model checker for timed automata
- Co-developed at Uppsala (S) and Aalborg (DK) by (Wang Yi, Kim Larsen et al)
- Integrated tool, graphical modeling interface
- Validation (simulation) and verification (model checking)
- Networks of timed automata
- Expressive and powerful language
- TA models prone to state space explosion problem
- <http://www.uppaal.com>

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Method: Uppaal (2)



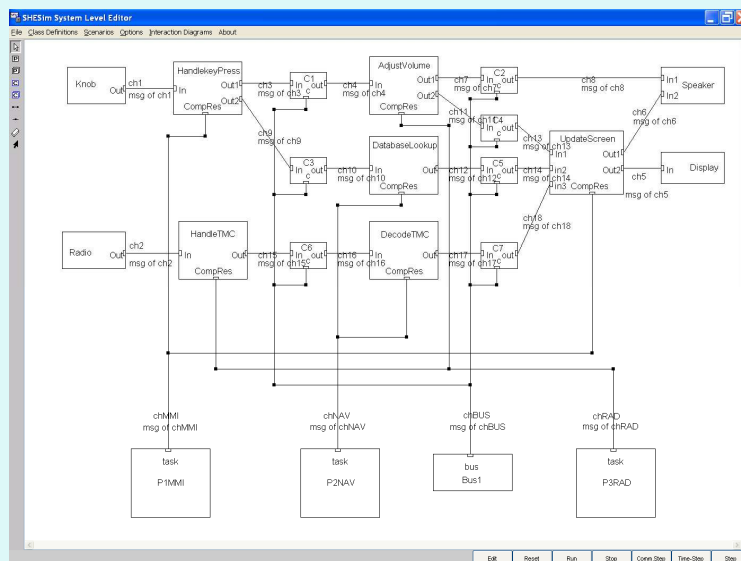
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Method: POOSL (1)

- Parallel Object-Oriented Specification Language
- Languages combines primitives for specifying data manipulations, concurrency and timing
- SHE method: Software / Hardware Engineering
- SheSIM tool for model construction and simulation
- Rotalumis for high-speed batch-oriented simulation
- Formal semantics based on probabilistic timed labeled transition systems
- Symbolic execution
- <http://www.es.ele.tue.nl/poosl/>

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Method: POOSL (2)



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- Played with many environment models
 - Pure periodic with zero offset (synchronous)
 - Pure periodic with fixed offset (synchronous)
 - Pure periodic with unknown offset (asynchronous)
 - Periodic with jitter ($j \leq p$)
 - Periodic with bursts ($j = 2p, d = 0$)
 - Sporadic (periodic with only upper bound to period)

- Some results easy to verify by hand
 - AddressLookup is fully independent and has highest priority
 - ChangeVolume is only dependent on itself

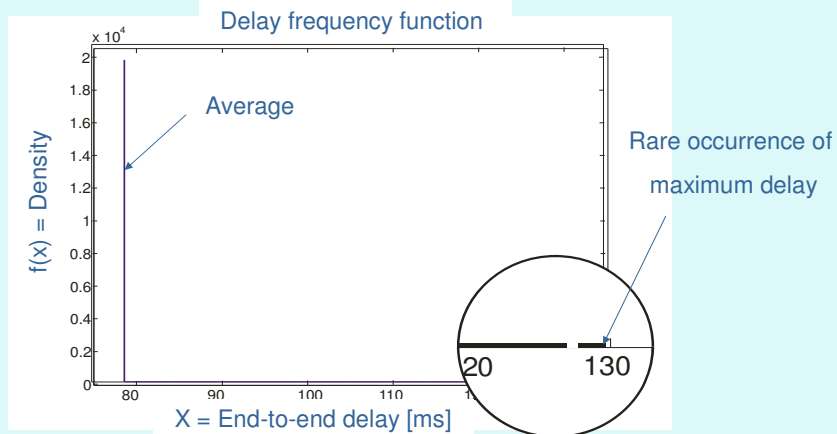
Table 1. Uppaal worst-case response time analysis results (in milliseconds)

<i>Requirement</i> \ <i>Event model</i>	<i>po</i> ($F = 0$)	<i>pno</i>	<i>sp</i>	<i>pj</i> ($J = P$)	<i>bur</i> ($J = 2P, D = 0$)
HandleTMC (+ ChangeVolume)	357.133	381.632	382.076	> 400.000 (df)	> 500.000 (rdf)
HandleTMC (+ AddressLookup)	172.106	239.080	239.080	329.989	420.898
K2A (ChangeVolume + HandleTMC)	27.716	27.716	27.716	> 27.715 (bf)	> 27.715 (bf)
A2V (ChangeVolume + HandleTMC)	41.796	41.796	41.796	> 41.795 (bf)	> 41.795 (bf)
AddressLookup (+ HandleTMC)	79.075	79.075	79.075	79.075	79.075

Table 2. Worst-case response time results – comparison with other tools

<i>Requirement</i> \ <i>Tool</i>	<i>Uppaal</i> (<i>po</i>)	<i>Uppaal</i> (<i>pno</i>)	<i>POOSL</i> (<i>pno</i>)	<i>SymTA/S</i> (<i>pno</i>)	<i>MPA</i> (<i>pno</i>)
HandleTMC (+ ChangeVolume)	357.133	381.632	266.94	382.086	390.0862
HandleTMC (+ AddressLookup)	172.106	239.080	244.26	253.304	265.8491
K2A (ChangeVolume + HandleTMC)	27.716	27.716	27.7067	27.717	28.1616
A2V (ChangeVolume + HandleTMC)	41.796	41.796	41.7771	41.798	42.2424
AddressLookup (+ HandleTMC)	79.075	79.075	78.8989	79.076	84.066

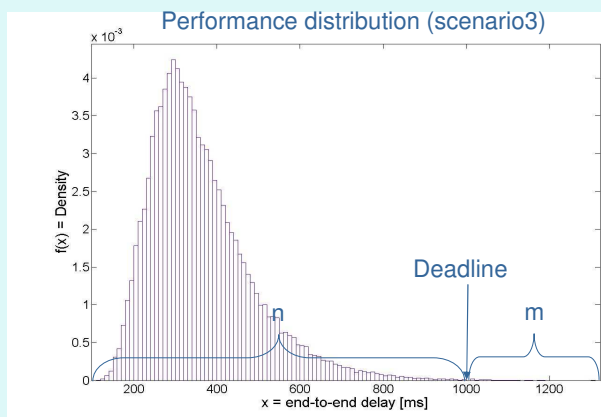
Results – POOSL case (1) Embedded Systems INSTITUTE



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Results – POOSL case (2) Embedded Systems INSTITUTE

- Probability of deadline misses
- More suitable design tradeoffs
- Accuracy depends on simulation length
 - Distribution fit



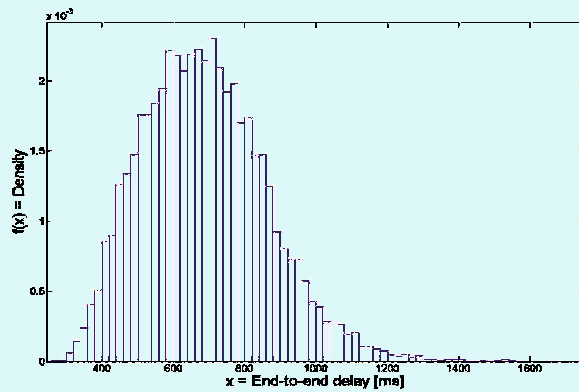
$$P(x < \text{deadline}) = \frac{m}{n + m}$$

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Results – POOSL case (2)

- Probability of deadline misses
- More suitable design tradeoffs
- Accuracy depends on simulation length
 - Distribution fit

Performance distribution (scenario 3)

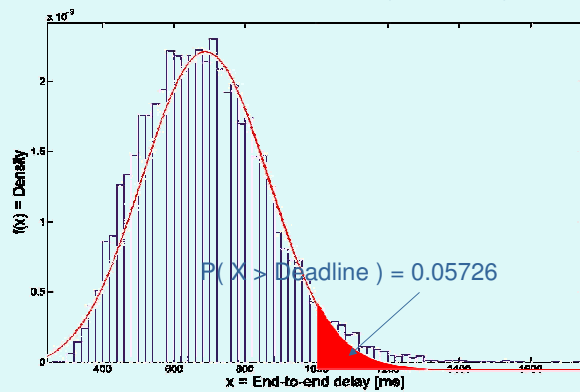


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Results – POOSL case (2)

- Probability of deadline misses
- More suitable design tradeoffs
- Accuracy depends on simulation length
 - Distribution fit

Performance distribution (scenario 3)



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- **Comparing results is as hard as getting the results**
 - Did we *really* model the same thing?
 - Simulation / computation effects or true “problem”?
 - Interaction with experts is needed to make comparison!
- **Methods are typically**
 - Either biased towards application domain; can cause mismatch
 - Or very generic; can cause huge modeling effort
- **Methods can be used complementary**
 - Provide answers to different types of questions
 - Model validation by moving to another paradigm

Questions?

<http://www.esi.nl/boderc>
<http://www.ee.ethz.ch/~leiden05>

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